![Texas Instruments logo] **TEXAS INSTRUMENTS**

SCOPE™
System Controllability/Observability
Partitioning Environment

Errata

October 1992   **General Purpose Logic Products**

This errata booklet contains changes to the SCOPE™ (System Controllability/Observability Partitioning Environment) Product Information Book, October 1992, Literature No. SSYV001.

Page 1-7, test architecture, last sentence of first paragraph

Replace "NO TAG" with "Figure 1".

Page 1-8, state diagram description, second sentence of first paragraph

Replace "NO TAG" with "Figure 1".

Page 1-10, instruction register description, first sentence of second paragraph

Replace "NO TAG" with "Table 3".

Page 1-11, boundary control register, last sentence of third paragraph

Replace "NO TAG" with "Table 2".

Page 1-13, instruction register opcode description, first sentence

Replace "NO TAG" with "Table 3".

Page 1-14, PSA input masking, fourth sentence

Replace "NO TAG" with "Table 2".

Page 1-19, timing description, last sentence of first paragraph

Replace "NO TAG" with "Figure 1".

Page 1-19, timing description, first sentence of second paragraph

Replace "Figure 1" with "Figure 12".

Pages 1-73, 1-83, 1-93, and 1-103

- In the last bulleted item in the right column, replace "(W)" with "(HV)".
- In the device designator centered above the pinout, replace "W" with "HV".

Page 1-113

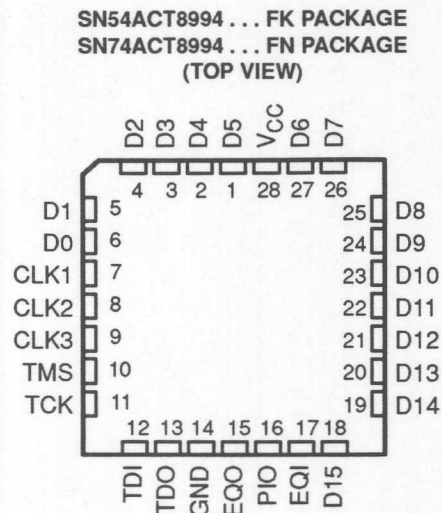- In the last bulleted item in the right column, replace "(W)" with "(HV)".

Page 1-114, SN54ACT8990 Pinout

- In the device designator centered above the pinout, replace "W" with "HV".
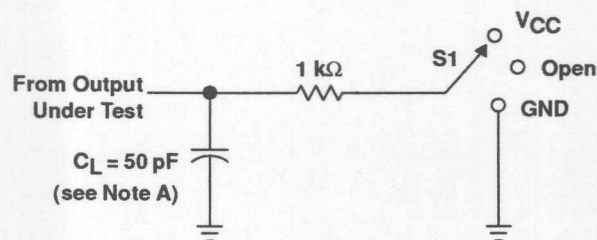
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Page 1-123, Device Pinout, FK and FN Packages

The correct pinout is as follows:

SN54ACT8994 . . . FK PACKAGE
SN74ACT8994 . . . FN PACKAGE
(TOP VIEW)

Page 1-133, PARAMETER MEASUREMENT INFORMATION, Figure 1 (Load Circuit and Voltage Waveforms)

The correct LOAD CIRCUIT FOR OUTPUTS is as follows:

LOAD CIRCUIT FOR OUTPUTS

Page 1-142, Table 2. Instruction Register Opcodes

- On the ninth row (BINARY CODE = 1000 1000), replace "Counter" in the SELECTED DATA REGISTER column with "Bypass".

- On the tenth row (BINARY CODE = 0000 1001), replace "Counter" in the SELECTED DATA REGISTER column with "Bypass".

Page 1-167, Table 2. Instruction Register Opcodes

- On the ninth row (BINARY CODE = 1000 1000), replace "Counter" in the SELECTED DATA REGISTER column with "Bypass".

- On the tenth row (BINARY CODE = 0000 1001), replace "Counter" in the SELECTED DATA REGISTER column with "Bypass".

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